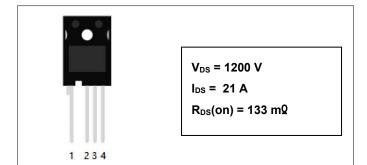


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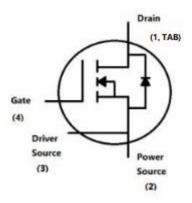
## S2M0120120K

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## S2M0120120K 1200V SIC POWER MOSFET



#### **Circuit Diagram**



#### Description

S2M0120120K is single SiC Power MOSFET packaged in TO-247-4 case. The device is a high voltage n-channel enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0120120K is ideal for energy sensitive, high frequency applications in challenging environments.

#### Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) = 133 mQ .
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin

#### Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

#### Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	V <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>DS</sub> = 100uA, T <sub>C</sub> = 25°C	1200	V
Gate Source Voltage	$V_{GSS}$	T <sub>c</sub> = 25 ° C, Absolute maximum values, AC (f>1Hz)	-10 to +25	V
Gate Source Voltage	V <sub>GSOP</sub>	T <sub>c</sub> = 25°C Recommended Operational Values	-5 to +20	V
Continuous Drain Current	ID	V <sub>GS</sub> = 20V, T <sub>C</sub> = 25°C	21	А
	ID	V <sub>GS</sub> = 20V, T <sub>C</sub> = 100°C	15	А
Pulsed Drain Current	I <sub>D,pulse</sub>	Tc=25°C	66	A
Power Dissipation	PD	T <sub>c</sub> =25°C	156	W

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### Electrical Characteristics(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Min.	Тур.	Max.	Unit s	
Drain Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 100 uA	1200			V	
		$V_{DS} = V_{GS}, I_D = 3.3 \text{ mA}$	2.0	2.9	4	V	
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 3.3 mA, T <sub>J</sub> = 175 °C		1.9		V	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 1200 V, V <sub>GS</sub> = 0 V		1	100	uA	
Gate Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0 V			250	nA	
Drain Source On-State	D	V <sub>GS</sub> = 20 V, I <sub>D</sub> = 13.3 A		133	150	mΩ	
Resistance	$R_{DS(on)}$	V <sub>GS</sub> = 20 V, I <sub>D</sub> = 13.3 A, T <sub>J</sub> = 175 °C		212		mΩ	
Transconductance		V <sub>DS</sub> = 20 V, I <sub>D</sub> = 13.3 A		5		s	
	gfs	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 13.3 A, T <sub>J</sub> = 175 °C		2		s	
Input Capacitance	CISS	$V_{GS} = 0 V,$ $V_{DS} = 1000 V$ $V_{AC} = 25 mV$ f = 100 kHz		652		pF	
Output Capacitance	Coss			47.6			
Reverse Transfer Capacitance	Crss			3.47			
Coss Stored Energy	Eoss			28		uJ	
Turn-On Switching Energy	E <sub>ON</sub>	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -5/+20 V		62.3			
Turn-Off Switching Energy	E <sub>OFF</sub>	ID =13.3 A, RG(ext)=2.5 Ω		62.7		uJ	
Turn-On Delay Time	$\mathbf{t}_{d(on)}$			3.5			
Rise Time	tr			6.7			
Turn-Off Delay Time	$t_{d(off)}$	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -5/20 V I <sub>D</sub> = 13.3 A, R <sub>G(ext)</sub> = 2.5 Ω, R <sub>L</sub> =80 Ω		8.3		ns	
Fall Time	t <sub>f</sub>			10.6			
Internal Gate Resistance	R <sub>G(int)</sub>	f = 1MHz, VAC = 25 mV, D-S short		6.4		Ω	
Gate to Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -5/20 V		12.8			
Gate to Drain Charge	$Q_{gd}$	I <sub>D</sub> = 13.3 A		6.0		nC	
Total Gate Charge	Qg			29.6		]	



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#### **Reverse Diode Characteristics:**

Characteristics	Symbol	Condition	Тур.	Max.	Units
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 6.7 A	3.7		V
	V <sub>SD</sub>	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 6.7 A, T <sub>J</sub> = 175 °C	3.3		V
Continuous Diode Forward Current	ls	V <sub>GS</sub> = -5 V, T <sub>C</sub> = 25 ℃	20		А
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 13.3 A, T <sub>J</sub> = 25 °C	7.3		ns
Reverse Recovery Charge	Qrr	V <sub>R</sub> = 800 V	0.05		uC
Peak Reverse Recovery Current	I <sub>mm</sub>	dif/dt= 3030 A/µs	11.9		А

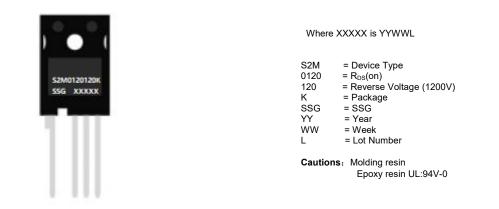
#### **Thermal-Mechanical Specifications:**

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T <sub>stg</sub>	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	$R_{ ext{ heta}JC}$	DC operation	0.96	°C/W
Maximum Thermal Resistance Junction to Ambient	R <sub>0JA</sub>		53	°C/W

#### **Ordering Information:**

Device	Package	Shipping
S2M0120120K	TO-247-4	30pcs/tube

#### **Marking Diagram**



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V<sub>GS</sub> = 20V

V<sub>GS</sub> = 18V

V<sub>GS</sub> = 16V

V<sub>GS</sub> = 10V

6 7 8 9 10 11 12 13 14

V<sub>DS</sub>, Drain to Source Voltage (V)

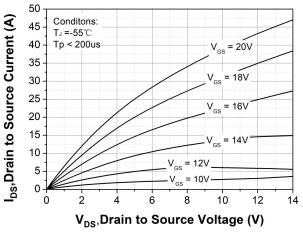
V<sub>GS</sub> = 14V

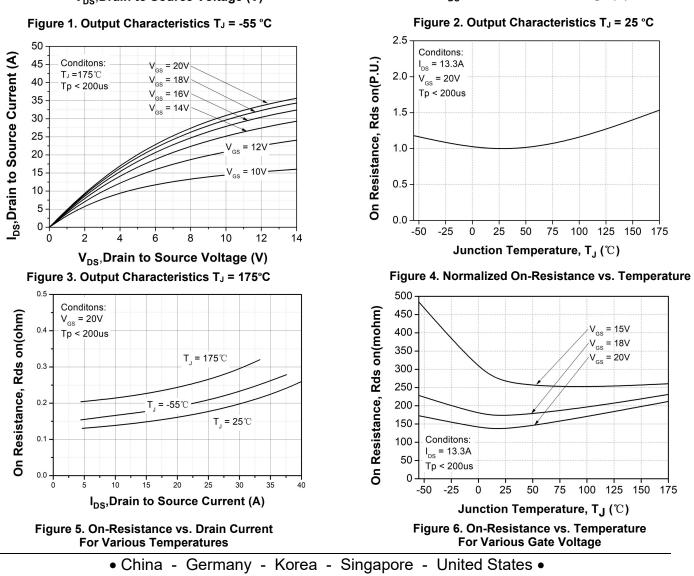
V<sub>GS</sub> = 12V

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#### **Ratings and Characteristics Curves**





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50

45

40

35

30

25

20

15

10

5

0

0

1

2 3 4 5

Conditons:

Tp < 200us

T<sub>J</sub> =25℃

l<sub>bs</sub>,Drain to Source Current (A)





V<sub>6sTH</sub>, Threshold Voltage(V)

3

2

-50 -25

Ó

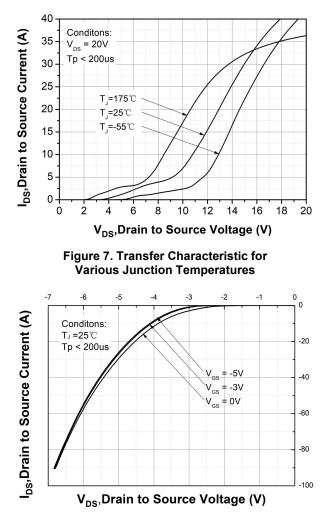


Figure 9. Body Diode Characteristic at T<sub>J</sub> = 25 °C

Conditons:

 $V_{_{\rm GS}} = V_{_{\rm DS}}$ 

100 125 150 175

Tp < 200us

I<sub>DS</sub> = 3.33mA

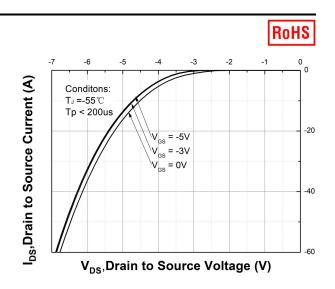


Figure 8. Body Diode Characteristic at T<sub>J</sub> = -55 °C

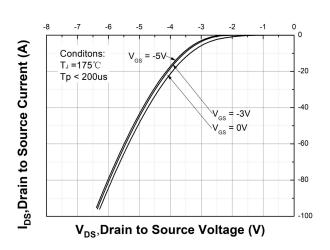
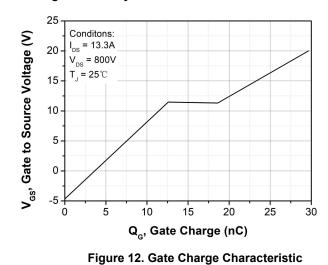


Figure 10. Body Diode Characteristic at T<sub>J</sub> = 175 °C





50 75

Junction Temperature,  $T_{I}$  (°C)

25

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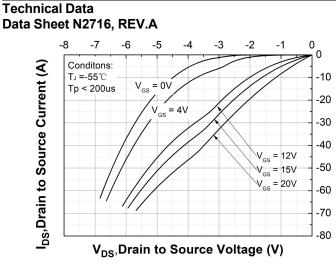


Figure 13. 3rd Quadrant Characteristic at T<sub>J</sub> = -55 °C

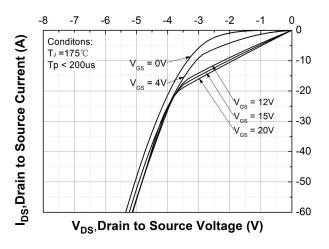


Figure 15. 3rd Quadrant Characteristic at T<sub>J</sub> = 175°C

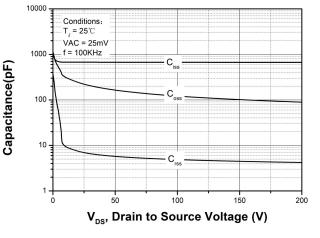


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

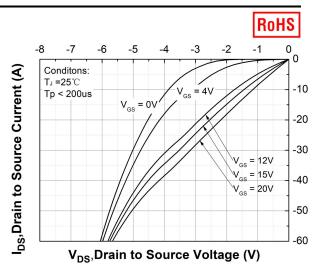


Figure 14. 3rd Quadrant Characteristic at T<sub>J</sub> = 25 °C

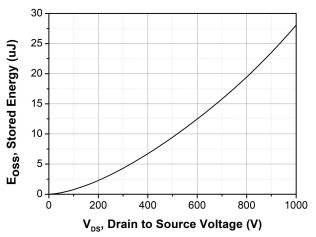
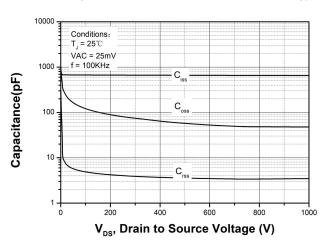
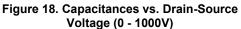


Figure 16. Output Capacitor Stored Energy

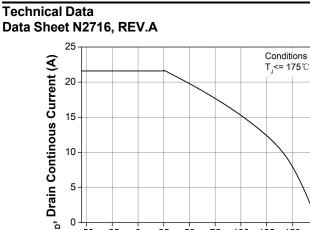




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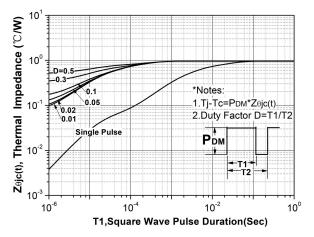
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-50 -25 0 25 50 75 100 125 150 T<sub>\_</sub>,Case temperature(℃)

175







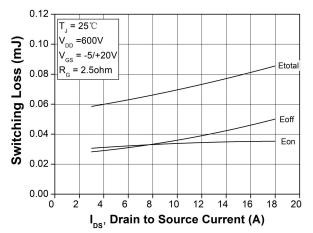


Figure 23. Clamped Inductive Switching Energy vs. Drain Current (V<sub>DD</sub> = 600V)

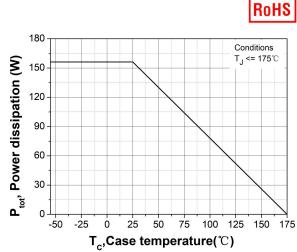


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

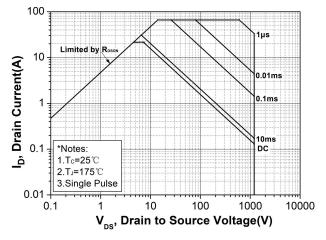


Figure 22. Safe Operating Area

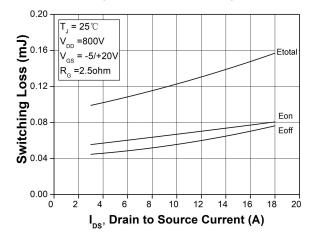


Figure 24. Clamped Inductive Switching Energy vs. Drain Current (V<sub>DD</sub> = 800V)

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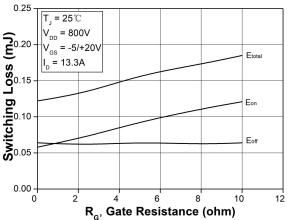


Figure 25. Clamped Inductive Switching Energy vs. R<sub>G(ext)</sub>

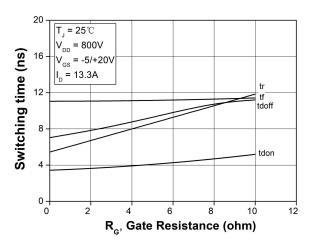


Figure 27. Switching Times vs. R<sub>G(ext)</sub>

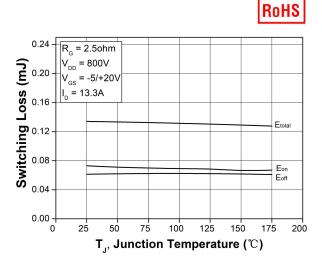


Figure 26. Clamped Inductive Switching Energy vs. Temperature

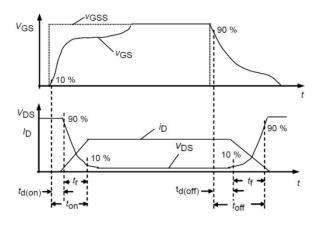


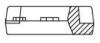
Figure 28. Switching Times Definition

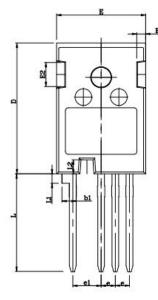
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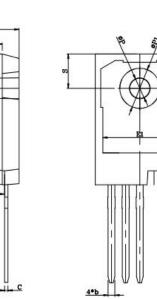


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#### **Mechanical Dimensions TO-247AD**







SYMBOL -	mm				
	Min	Nom	Max		
A	4.80	5.00	5.20		
Al	2.23	2.41	2.59		
A2	1.85	2.00	2.15		
b	1,11	1.21	1.36		
b1	2.35	2.55	2.75		
с	0.51	0.61	0.75		
D	23.30	23.45	23.60		
Dl	16.25	16.55	16.85		
Е	15.75	15.94	16.10		
El	13.00	13.26	13.43		
E2	4.00	4.30	4.60		
E3	1.15	1.45	1.75		
e		2.54BSC			
el		5.08BSC			
L	17.31	17.47	17.82		
L1	1.50	1.70	1.90		
ØP	3.51	3.60	3.65		
ØP1	7.08	7.19	7.30		
S	6.15BSC				





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